



**Ref No : IJRTI / Vol 11 / Issue 4 / 161**

**To,  
M.V.Sai Dharahas**

**Subject:** Publication of paper at International Journal for Research Trends and Innovation.

Dear Author,

With Greetings we are informing you that your paper has been successfully published in the International Journal for Research Trends and Innovation (ISSN: 2456-3315). Following are the details regarding the published paper.

About IJRTI : ISSN Approved - International Scholarly open access, Peer-reviewed, and Refereed Journal, Impact Factor: 8.14, (Calculate by google scholar and Semantic Scholar | AI-Powered Research Tool), Multidisciplinary, Monthly, Online, Print Journal, Indexing in all major database & Metadata, Citation Generator, Digital Object Identifier(DOI)

Registration ID : IJRTI\_ 211435

Paper ID : IJRTI2604161

Title of Paper : Efficient Sequential Circuit Design and Implementation Using 7nm PTM

Impact Factor : 8.14 (Calculate by Google Scholar) | License by Creative Common 3.0

DOI :

Published in : Volume 11 | Issue 4 | April-2026

Page No : b168-b175

Published URL : <https://ijrti.org/viewpaperforall.php?paper=IJRTI2604161>

Authors : M.V.Sai Dharahas, K.Vilson, Y.Stephen, C V P Supradeepthi

Thank you very much for publishing your article in IJRTI.

Editor In Chief  
International Journal for Research Trends and Innovation  
(ISSN: 2456-3315)

