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The Board of  
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Is hereby awarding this certificate to

**K.Rushendrababu**

In recognition of the publication of the paper entitled

**Design and Simulation of a 32bit Ultra Low Power SAR ADC with Digital  
calibration Using 180nm CMOS Technology**

Published in Volume 11 Issue 3, March-2026

*Co-Authors - T. Naga Malleswari, V. Anookhya, T.  
Santhosh Kumar, M. Sita Sri*

**Paper ID - IJRTI2603109**

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